



Patent  
Attorney's Docket No. H1105D

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )  
Shibly S. AHMED, et al. ) Group Art Unit: 2812  
Application No.: 10/602,061 ) Examiner: Unassigned  
Filed: June 24, 2003 )  
For: DOUBLE GATE SEMICONDUCTOR )  
DEVICE HAVING SEPARATE GATES )

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**  
**TRANSMITTAL LETTER**

U.S. Patent and Trademark Office  
2011 South Clark Place  
Customer Window, Mail Stop Amendment  
Crystal Plaza Two, Lobby, Room 1B03  
Arlington, Virginia 22202

Sir:


Enclosed is a Supplemental Information Disclosure Statement and accompanying form  
PTO-1449 for the above-identified patent application.

- ☒ No additional fee for submission of the IDS is required.
- ☐ The fee of \$180.00 as set forth in 37 C.F.R. § 1.17(p) is also enclosed.
- ☐ A certification under 37 C.F.R. § 1.97(e) is also enclosed.
- ☐ Charge \$ \_\_\_\_\_ to Deposit Account No. 50-1070 for the fee due.
- ☐ A check in the amount of \$ \_\_\_\_\_ is enclosed for the fee due.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R.  
§§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to  
Deposit Account No. 50-1070.

Respectfully submitted,

HARRITY & SNYDER, L.L.P.

By:   
Glenn Snyder  
Reg. No. 41,428

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Customer Number: 26615

Date: June 4, 2004



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Title: DOUBLE GATE SEMICONDUCTOR	)	
DEVICE HAVING SEPARATE GATES	)	

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 C.F.R. § 1.97(b)**

U.S. Patent and Trademark Office  
2011 South Clark Place  
Customer Window, Mail Stop Amendment  
Crystal Plaza Two, Lobby, Room 1B03  
Arlington, Virginia 22202

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), Applicant(s) bring to the attention of the Examiner the documents listed on the attached PTO 1449. This Supplemental Information Disclosure Statement is being filed before the mailing date of a first Office Action in the above-referenced application. As such, no certification or fee is required. Copies of the listed documents are attached.

Applicant(s) respectfully request(s) that the Examiner consider the listed documents and indicate that they were considered by making appropriate notations on the attached form.

If any copending application(s) is/are cited on the attached PTO 1449, the Examiner's attention is directed to the foregoing application(s) in compliance with § 2001.06(b) of the Manual of Patent Examining Procedure. By identifying the copending application(s), the assignee and/or applicant of the application(s) do not waive confidentiality of the application(s). Accordingly, the U.S. Patent and Trademark Office is requested to maintain the confidentiality of the copending application(s) under 35 U.S.C. § 122.

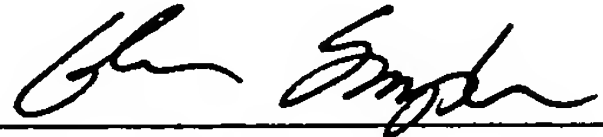
This submission does not represent that a search has been made and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claim in the application and Applicant(s) determine(s) that the cited document(s) do not constitute "prior art" under United States law, Applicant(s) reserve(s) the right to present to the Office the relevant facts and law regarding the appropriate status of such documents.

Applicant(s) further reserve(s) the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 50-1070.

Respectfully submitted,

HARRITY & SNYDER, L.L.P.

By:   
Glenn Snyder  
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Suite 300  
Fairfax, Virginia 22030  
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<b>INFORMATION DISCLOSURE CITATION</b> PTO-1449	Customer Number <b>26615</b>	ATTORNEY'S DKT No. H1105D	APPLICATION No. 10/602,061
		APPLICANT(S) Shibly S. AHMED et al.	
		FILING DATE June 24, 2003	GROUP Unassigned

**U.S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	5,563,082	10-08-96	Mukai	437	41	12-23-94
	6,413,802 B1	07-02-02	Hu et al.	438	151	10-23-00

**FOREIGN PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
	Leland Chang et al.: "Gate Length Scaling and Threshold Voltage Control of Double-Gate MOSFETs," IEDM, December 10, 2000, pages 719-722.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

Inventor(s): Shibly S. Ahmed et al.

Appln. No.: 10/602,061

Docket No.: H1105D (0020-0068DIV1)

Working Atty: GS

Date: June 4, 2004

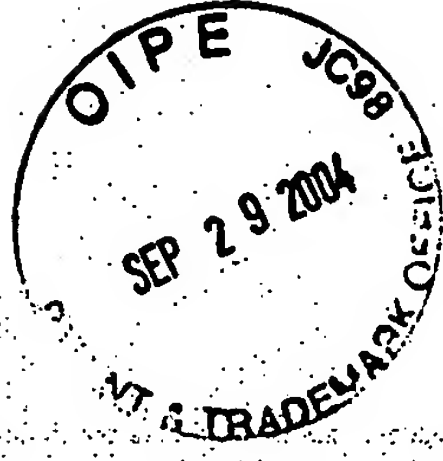
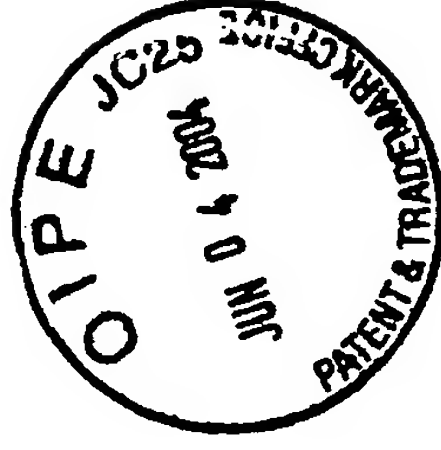
Title: DOUBLE-GATE SEMICONDUCTOR DEVICE HAVING SEPARATE GATES

The following was/were received in the U.S. Patent and Trademark Office on the date stamped hereon:

- ☒ SUPPLEMENTAL INFORMATION DISCLOSURE  
STATEMENT TRANSMITTAL LETTER
- ☒ SUPPLEMENTAL INFORMATION DISCLOSURE  
STATEMENT UNDER 37 CFR 1.97(b)
- ☒ INFORMATION DISCLOSURE CITATION & 3 REFS

Customer Number:

**26615**



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